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PATENT APPLICATION
Attorney's Do. No. 2522-13

jc872 U.S. PRO
10/076237



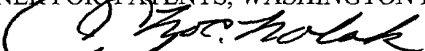
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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I HEREBY CERTIFY THAT THIS PAPER AND ENCLOSURES AND/OR FEE ARE BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE "EXPRESS MAIL POST OFFICE TO ADDRESSEE" SERVICE UNDER 37 CFR 1.10 ON THE DATE INDICATED ABOVE AND IS ADDRESSED TO: BOX PATENT APPLICATION, ASSISTANT COMMISSIONER FOR PATENTS, WASHINGTON D.C. 20231.

ADRIENNE CHOCHOLAK
(SENDER'S PRINTED NAME)


(SIGNATURE)

Box Patent Application
Assistant Commissioner for Patents
Washington, D.C. 20231

Enclosed for filing is a patent application under 37 CFR 1.53(b) of:

Inventor Tae-Ho JANG:
For: SILICON-ON-INSULATOR (SOI) SUBSTRATE AND METHOD FOR
 MANUFACTURING THE SAME

Applicant requests FIG. 3C to be published with the application.

Enclosures:

- | | | |
|-------------------------------------|---|--|
| <input checked="" type="checkbox"/> | Specification (pages 1-8); claims (pages 9-11); abstract (page 12) | |
| <input checked="" type="checkbox"/> | TEN sheet(s) of FORMAL drawings | |
| <input checked="" type="checkbox"/> | Declaration or Combined Declaration and Power of Attorney | |
| <input checked="" type="checkbox"/> | Newly executed (original or copy) | |
| <input checked="" type="checkbox"/> | Assignment with cover sheet | |
| | Assignee Name and Address: | Samsung Electronics Co., Ltd.
Suwon-city, Kyungki-do, Korea |
| <input checked="" type="checkbox"/> | Certified copy of Priority Document No. 2001-08111, filed February 19, 2001 | |
| <input checked="" type="checkbox"/> | Return Postcard | |

<u>CLAIMS AS FILED</u>				
For	Number Filed	Number Extra	Rate	Basic Fee \$740
Total Claims	18-20	0	x \$ 18 =	0
Independent Claims	2-3	0	x \$ 84 =	0
TOTAL FILING FEE				\$740



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Any deficiency or overpayment should be charged or credited to deposit account
number 13-1703. A duplicate copy of this sheet is enclosed.



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Respectfully submitted,

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